Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1 (currently amended): An image-capturing apparatus with error-detecting function, the image-capturing apparatus comprising:

a light sensor for sensing light reflected from an image and for transforming the light into an analog image signal;

an analog front-end device electrically connected to the light sensor for transforming the analog image signal into a digital image signal;

a register electrically connected to the analog front-end device for storing the digital image signal transformed by the analog front-end device;

an encoder electrically connected to the ~~analog front-end device~~ register for encoding the digital image signal ~~transformed by the analog front-end device~~ stored in the register;

a decoder for decoding the encoded digital image signal encoded by the encoder;

a processor electrically connected to the decoder for determining whether the encoded digital image signal encoded by the encoder is correct or not and for generating a control signal to control the ~~operations of the light sensor and the~~ encoder to re-encode the digital image signal stored in the register when the processor determines that the encoded digital image signal encoded by the encoder is not correct; and

a signal transmission device electrically connected between the light sensor, the decoder, and the processor for transmitting the encoded digital image signal encoded by the encoder and the control signal generated by the processor.

2-4 (cancelled)

5 (currently amended): The image-capturing apparatus of ~~claim 4~~ claim 1, wherein every time the analog front-end device transforms a new analog image signal

into a new digital image signal, the analog front-end device updates the digital image signal stored in the register with the new digital image signal.

6 (cancelled)

7 (original): The image-capturing apparatus of claim 1, wherein the encoder, the decoder, and the processor form an odd parity error-checking mechanism.

8 (original): The image-capturing apparatus of claim 1, wherein the encoder, the decoder, and the processor form an even parity error-checking mechanism.

9 (original): The image-capturing apparatus of claim 1, wherein the encoder, the decoder, and the processor form a cyclic-redundancy error-checking (CRC) mechanism.

10 (currently amended): The image-capturing apparatus of claim 1, wherein the digital image signal comprises N bits, and the encoded digital image signal encoded by the encoder from the digital image signal comprises a check bit having a value set according to the N bits of the digital image signal and a predetermined error-checking mechanism formed according to the encoder, the decoder, and the processor, and N corresponding bit pairs, each of the bit pairs comprising an odd location bit and an even location bit equal to the odd location bit, and an odd location bit of an [[n_{th}]] $\underline{n^{th}}$ bit pair of the encoded digital image signal having a value equal to that of an [[n_{th}]] $\underline{n^{th}}$ bit of the digital image signal.

11 (original): The image-capturing apparatus of claim 10, wherein the control signal is a null signal.

12 (original): The image-capturing apparatus of claim 10, wherein the predetermined error-checking mechanism is an odd parity error-checking mechanism.

13 (original): The image-capturing apparatus of claim 10, wherein the predetermined error-checking mechanism is an even parity error-checking mechanism.

14 (original): The image-capturing apparatus of claim 10, wherein the predetermined error-checking mechanism is a cyclic-redundancy error-checking mechanism.

15 (original): The image-capturing apparatus of claim 1, wherein the processor is an application-specific integrated circuit (ASIC).

16 (original): The image-capturing apparatus of claim 1, wherein the light sensor is a charge-coupled device (CCD).

17 (original): The image-capturing apparatus of claim 1, wherein the analog front-end device is installed in a light engine comprising the light sensor.

18 (original): The image-capturing apparatus of claim 1, wherein the encoder is installed in a light engine comprising the light sensor.

19 (original): The image-capturing apparatus of claim 1, wherein the analog front-end device is installed on a motherboard.

20 (original): The image-capturing apparatus of claim 1, wherein the encoder is installed on a motherboard.